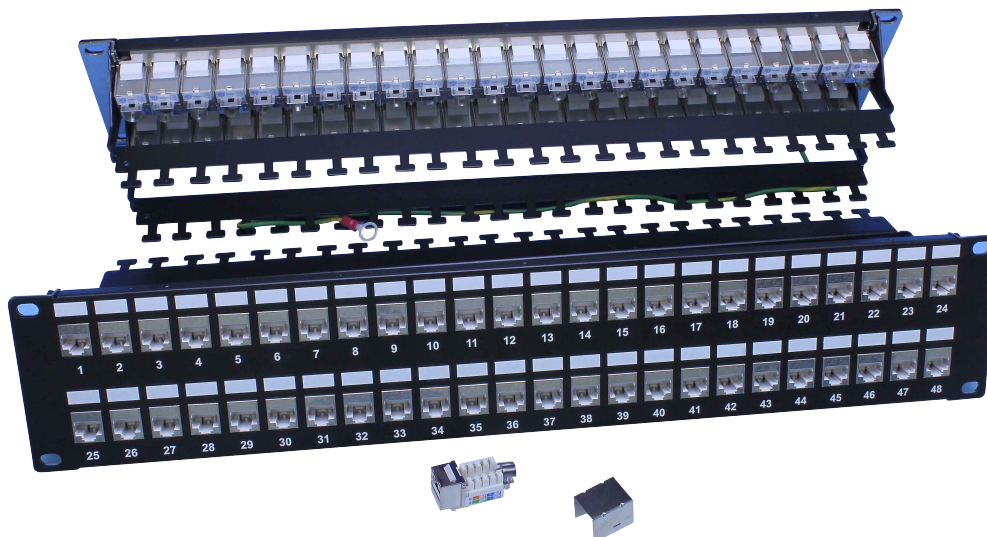


ON-EVPLKD-F480XMTRM C5e Vertical Keystone Jack Loaded, 19"-2U Patch Panel



Electrical Specifications

Insulation Resistance : 500M ohms (min.)

Contact Resistance : 20m ohms (max.)

Current Rating : 1.5 Amps.

DC Resistance : 0.1 ohms (max.)

Withstanding Voltage : 100 VAC RMS @ 60Hz / 1 min.

Mechanical Specifications

IDC Punch Down Cycle : Over 250 Cycles (min.)

Jack Inserion Cycle : Over 2000 Cycles (min.)

Operation Temperature : -40°C ~ +70°C

Performance

Meets C5e EIA/TIA 568B Standard

Materials

| | |
|------------------|---|
| Panel | Metal (SPCC) |
| Cable Management | Metal (SPCC) |
| Keystone Body | Fireproof ABS UL-94V0 |
| Metallic Case | Metal |
| Jack | 8P8C 50 micro inches gold plated |
| PCB | FR-4 / UL-94V0 |
| IDC Body | Fireproof ABS UL-94V0 |
| IDC Pin | Bronze & Tinned |
| IDC Description | For 22-26AWG. For 110 or Krone punch-down Tool |